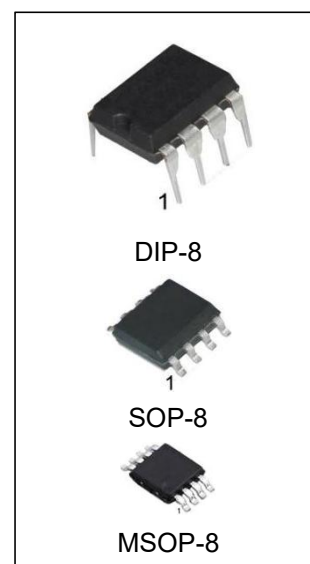


MC7660 Charge Pump DC-DC Voltage Converter

Features

- Converts +5V Logic supply to - 5V;
- Wide input voltage range:4V~9V;
- Efficient voltage conversion: 99.9%;
- Good power efficiency: 98%;
- Low power supply: 40uA @5V input;
- Easy to use: only two external capacitors required;
- Compatible with RS232 negative powersupply standard;
- High ESD protection: up to 3kV;
- No Dx diode needed for high voltage operation;
- Package : SOP-8, DIP-8,MSOP-8



Ordering Information

DEVICE	Package Type	MARKING	Packing	Packing Qty
MC7660N	DIP-8	MC7660	TUBE	2000pcs/Box
MC7660M/TR	SOP-8	MC7660	REEL	2500pcs/Reel
MC7660MM/TR	MSOP-8	7660	REEL	3000pcs/Reel

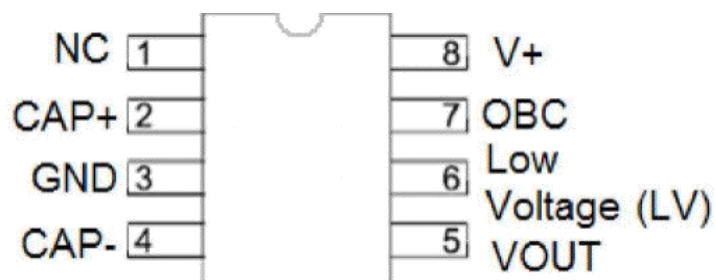
Description

MC7660 is a charge pump dc-to-dc voltage converter using AL-gate CMOS technology and optimization design. It converts a +4V to +9V input to a corresponding -4V to -9V output using only two external capacitors, eliminating inductors and their associated cost, size and EMI. The on-board oscillator operates at a nominal frequency of 10KHz. Operation below 10 KHz (for lower supply current applications) is possible by connecting an external capacitor from OSC to ground.

Applications

- LCD Display Module
- Specially designed LCD display module
- Instrument product

Pin Configuration

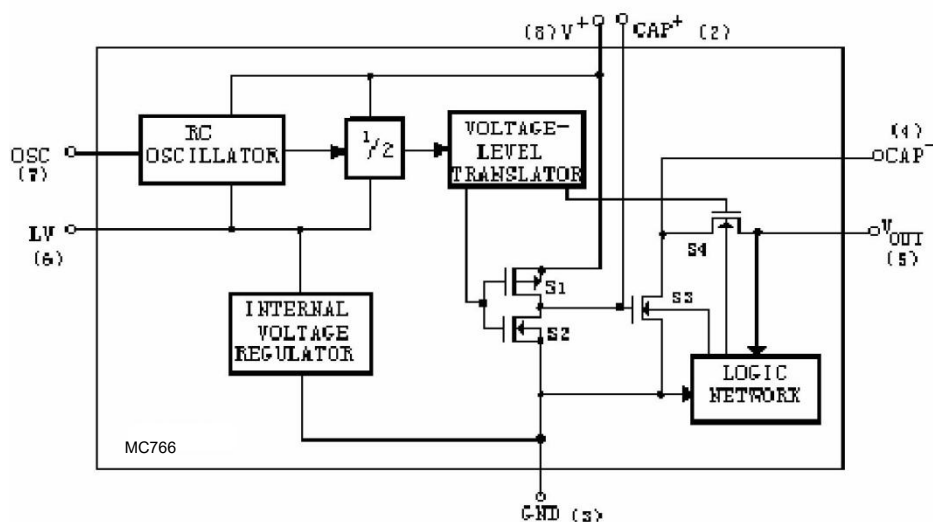


DIP-8/SOP-8/MSOP-8

Pin Assignment

PIN Number	PIN NAME	FUNCTION
SOP8/DIP8/MSOP8		
1	NC	No connection
2	CAP+	Connecting external capacitor(+) pin
3	GND	Ground pin
4	CAP-	Connecting external capacitor(-) pin
5	Vout	Voltage output pin
6	Low Voltage	Low voltage selection pin
7	OSC	Connecting oscillation capacitor pin
8	V+	Power supply pin

Block Diagram



Block of function module in chip

Absolute Maximum Ratings

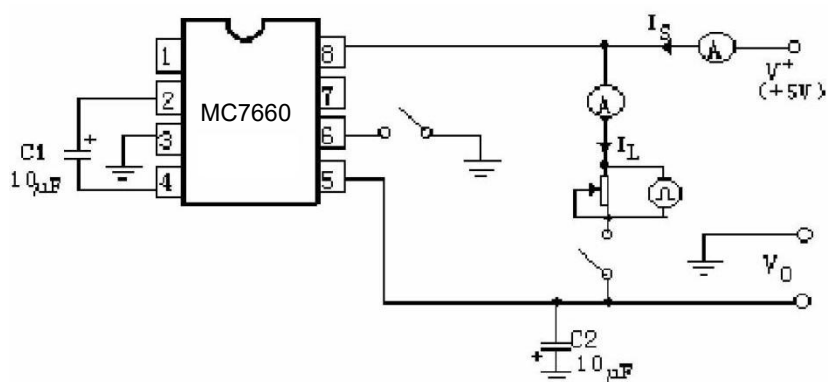
PARAMETER		SYMBOL	RATINGS	UNITS
Input voltage		V_{IN}	+9V	V
LV PIN input voltage		V_{LX}	-0.3 ~ (V++0.3) (if V+<5.5V) (V+-5.5V) ~ (V++0.3V) (if V+>5.5V)	V
OSC PIN input voltage		V_{OSC}		
LV PIN input voltage		I_{LV}	20 (if V+>3.5V)	μA
Output Short Duration (Vsupply $\leq$ 5.5V)			∞	
Continuous Total Power Dissipation (TA $\leq$ 75°C)	SOP8	Pd	470	mW
	DIP8	Pd	730	mW
Operating Ambient Temperature		T _{Opr}	-40~+85	°C
Storage Temperature		T _{stg}	-65~+150	°C
Lead Temperature (Soldering, 10 seconds)		T _L	260	°C

Note: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but specific performance is not ensured.

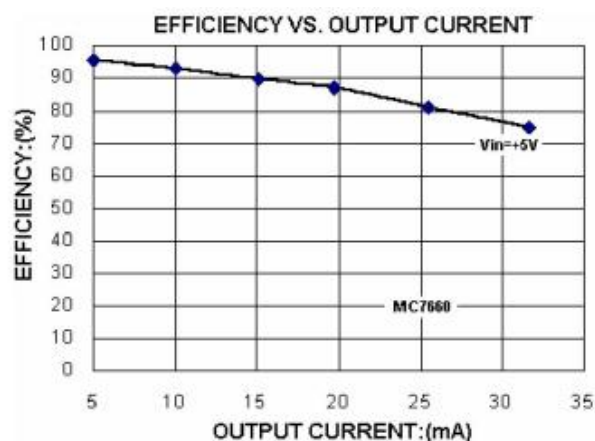
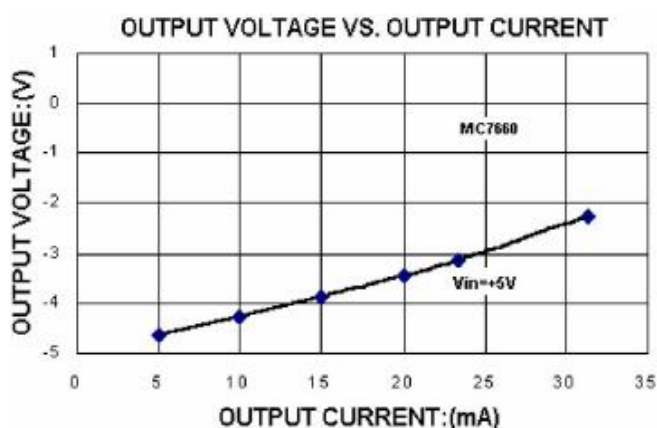
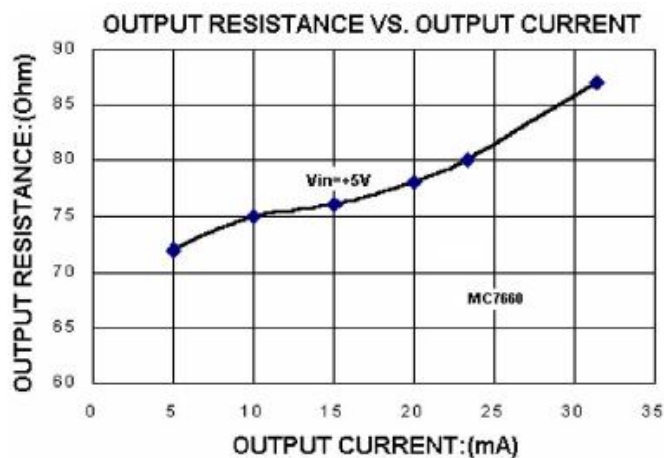
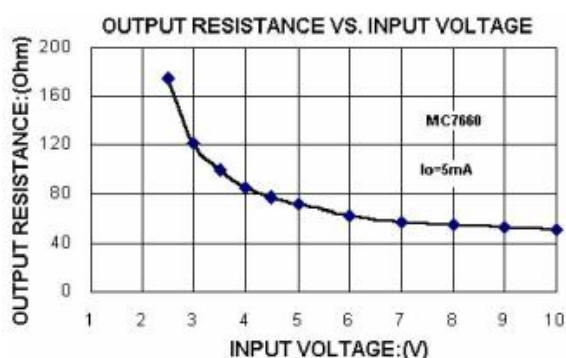
Electrical Characteristics (V+=5V, C_{OSC}=0)

Symbol	Item	Conditions	Min	Typ	Max	Unit
I ⁺	Supply Current	R _L = ∞	—	40	100	μA
V ⁺ _H	Supply Voltage, High	LV Open	4	—	8	V
V ⁺ _L	Supply Voltage, Low	LV to GND	2.5	—	3.5	V
R _{OUT}	Output Resistance	I _{OUT} =20mA T _A =25°C		70		Ω
		I _{OUT} =5mA V+=3V T _A =25°C		120		Ω
F _{OSC}	Oscillator Frequency	Pin 7 open	—	10	—	kHz
P _{EFF}	Power Efficiency	R _L =5k Ω	95	98	—	%
V _{OUTEFF}	Voltage Conversion Efficiency	R _L = ∞	98	99.9	—	%

Test Circuits



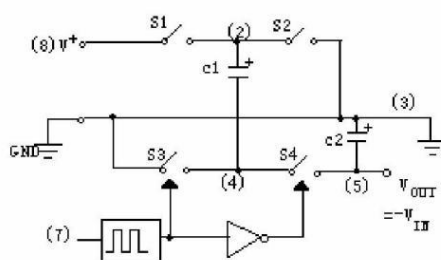
Type Characteristics



Operation

MC7660 together with two external capacitors C1、C2 complement a voltage Inverter. Capacitor C1 is charged to a voltage V_+ , for the first half cycle when switches S1 and S3 are closed (while switches S2 and S4 are open during this half cycle); During the second half cycle of operation, switches S2 and S4 are closed, with S1 and S3 open, thereby shifting capacitor C2 negatively to $-V_+$.

The voltage regulator portion of the chip is an integral part of the anti-latch-up circuitry. Its inherent voltage drop can degrade operation at low voltages. To improve low-voltage operation, the LV pin should be connected to GND, disabling the regulator. For supply voltages greater than 3.5V, the LV terminal must be left open to ensure latch-up-proof operation.



Charge Pump Voltage Converter operating process

Theoretical Power Efficiency Considerations

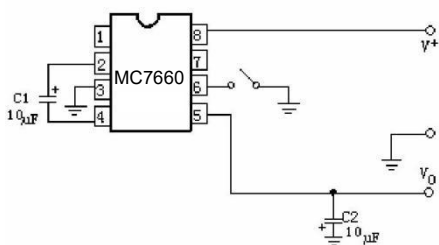
In theory, a capacitive charge pump can approach 100% efficiency if certain conditions are met:

- 1、 The drive circuitry consumes no power.
- 2、 The output switches have extremely low ON resistance and no offset when operation.
- 3、 The impedances of the pump and reservoir capacitors are negligible at the pump frequency.

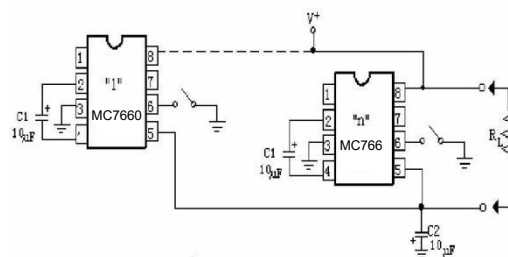
Notes:

- 1、 Supply voltage should not exceed maximum rating;
- 2、 Do not connect LV terminal to GND for supply voltages greater than 3.5V;
- 3、 Do not short circuit the output to V+ supply for voltages above 5.5V for extended periods;
- 4、 Polarized capacitors should be connected as the figure above.

Typical Application

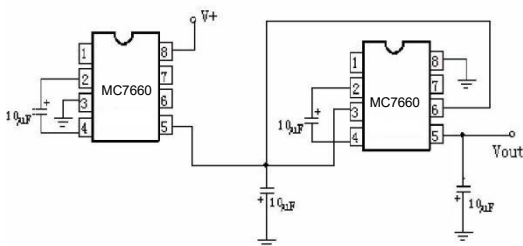


Above figure is the basic application circuit to provide a negative supply from -4V~ -9V while a positive supply from +4V ~ +9V is available. When $V^+ = +5V$, the output resistance is about $70\ \Omega$; The output voltage is -4V while the load current is 10mA.

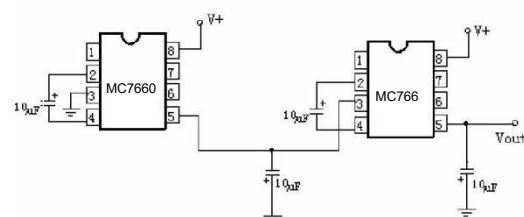


MC7660 may be paralleled to reduce output resistance (see the above figure)

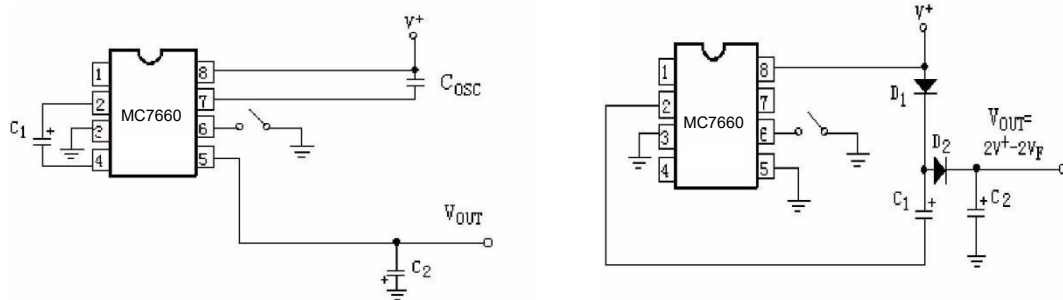
$$R_{out} = R_{out}(\text{of MC7660}) / n (\text{number of devices})$$



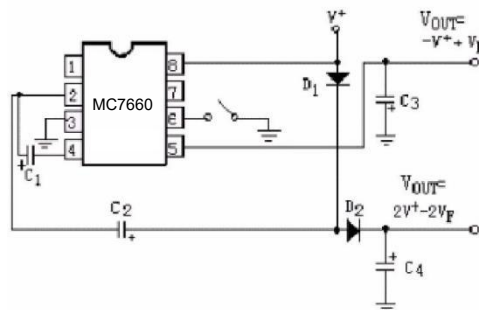
MC7660 may be cascaded as shown above to produce larger negative output voltage (-10V). However, The output resistance is approximately two of a single chip resistance.



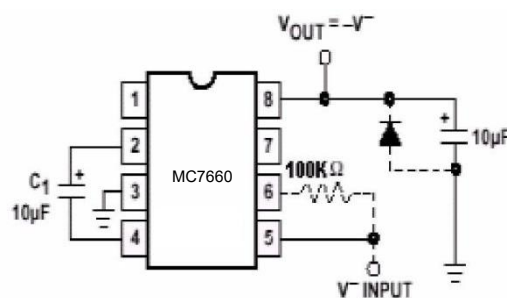
MC7660 may be cascaded as shown above to produce larger negative output voltage (-15V). However, The output resistance is approximately two of a single chip resistance.



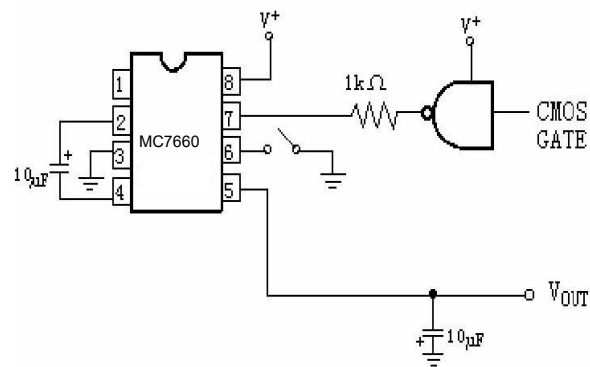
To increase the conversion efficiency, the oscillator frequency may be lowered by connecting a capacitor from pin 7 and pin 8 as shown above. This reduces the switching losses. However, lowering the oscillator frequency will cause an undesirable increase in the impedance of the pump and reservoir capacitors. So, the values of C1 and C2 should be increased by the same factor that the MC7660 may be applied to achieve positive voltage multiplication using the circuit shown in the above figure



In above figure, C1、C3 are the pump and reservoir capacitors respectively for the generation of the negative voltage; C2、C4 are the pump and reservoir capacitors respectively for the multiplied positive voltage. When +5V supply is provided, +9V and -5V can be generated.



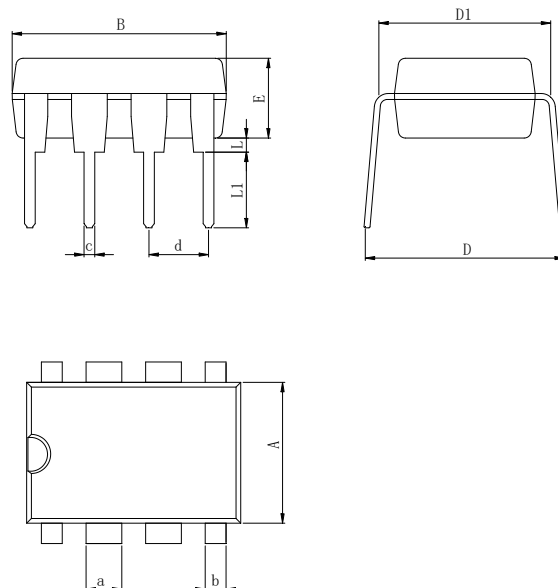
Since the switches that allow the charge pumping operation are bi-directional, the charge transfer can be performed backwards as easily as forwards. In the above figure, MC7660 transforming -5V to +5V. The only problem hear is that the internal clock and switch-drive section will not operate until some positive voltage has been generated. An initial inefficient pump, as show in left figure, could be used to start this circuit up, after which it will bypass the other (D1 and D2 in left figure would never turn on), or else the diode and resistor show dotted in above figure can be used to "force" the internal regulator on.



In some applications due to noise, it may be desirable to increase the oscillator frequency. This can be achieved by overdriving the oscillator from an external clock as shown in the above figure. The external clock output should connect a 1k Ω resistor to prevent device latch-up. Besides, the pump frequency will be half of the clock frequency because of the internal circuit

Physical Dimensions

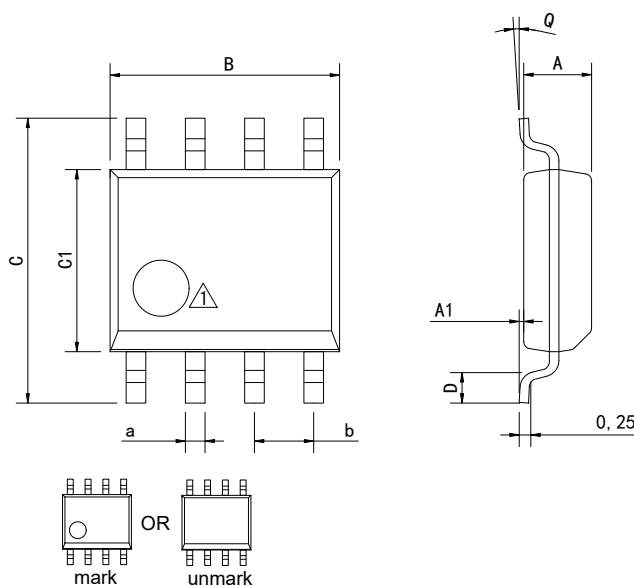
DIP-8



Dimensions In Millimeters(DIP-8)

Symbol:	A	B	D	D1	E	L	L1	a	b	c	d
Min:	6.10	9.00	8.10	7.42	3.10	0.50	3.00	1.50	0.85	0.40	2.54 BSC
Max:	6.68	9.50	10.9	7.82	3.55	0.70	3.60	1.55	0.90	0.50	

SOP-8



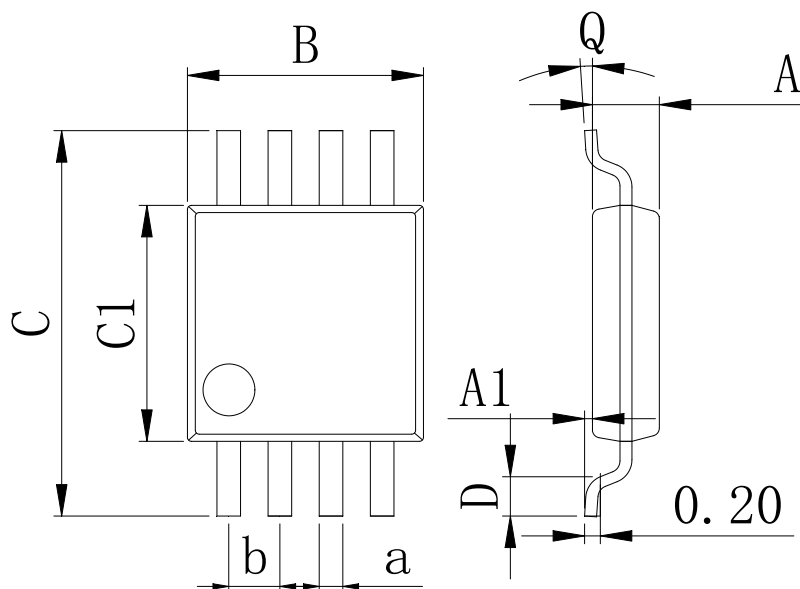
 Package top mark may be in lower left corner or unmark

Dimensions In Millimeters(SOP-8)

Symbol:	A	A1	B	C	C1	D	Q	a	b
Min:	1.35	0.05	4.90	5.80	3.80	0.40	0°	0.35	1.27 BSC
Max:	1.55	0.20	5.10	6.20	4.00	0.80	8°	0.45	

Physical Dimensions

MSOP-8



Dimensions In Millimeters(MSOP-8)									
Symbol:	A	A1	B	C	C1	D	Q	a	b
Min:	0.80	0.05	2.90	4.75	2.90	0.35	0°	0.25	0.65 BSC
Max:	0.90	0.20	3.10	5.05	3.10	0.75	8°	0.35	

Revision History

REVISION NUMBER	DATE	REVISION	PAGE
V1.0	2012-6	New	1-11
V1.1	2019-9	Update encapsulation type、Updated DIP-8 dimension	1、 9
V1.2	2024-11	Update Lead Temperature、 Update input voltage range 9V	1、 3
V1.3	2025-12	Update important statements、 Update SOP-8 Dimension drawing	9、 12

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